

ABSTRACT OF THE DISCLOSURE

Disclosed is an image processor with frame-rate conversion that can perform frame-rate conversion of a video signal with a single frame memory. In this image processor with frame-rate conversion, input digital video signals are successively written on the frame memory with a timing synchronized with a vertical synchronization signal included in the input digital video signals. During this time, a frequency signal that mainly consists of a train of  $N$  pulses for every  $M$  cycles of the vertical synchronization signal is generated as a vertical synchronization signal being rate-converted, and the input digital video signals stored in the frame memory are read out in the order in which they were written with a timing synchronized with the vertical synchronization signal being rate-converted. Such a configuration makes it possible to convert the input digital video signals to video signals having a desired vertical synchronization frequency with use of a single frame memory, thereby converting frame-rate.